

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Eliyahou Harari et al.		
Title:	Flash EEprom System With Programming Verification		
Application No.:	09/129,675	Filing Date:	August 5, 1998
Examiner:	Tran, Andrew Q.	Group Art Unit:	2824
Docket No.:	SNDK.006USS	Conf. No.:	4949

---

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Patent no. 5,172,338 and parent patent no. 5,991,517 are being asserted by their assignee SanDisk Corporation in a matter before the United States International Trade Commission, Investigation no. 337-TA-560. This Statement is being filed to bring information arising from this matter to the attention of the Examiner.

An "Expert Report of Dr. Richard Pashley . . ." includes opinions that certain claims of the '338 and '517 patents are invalid over the prior art and unenforceable due to inequitable conduct in obtaining their grant. Also included in this Statement are references discussed in this expert report that have not already been submitted in the present application. A "Rebuttal Expert Report of G.R. Mohan Rao, Ph.D. . . ." submitted on SanDisk's behalf in that matter rebuts the invalidity opinions of Dr. Pashley's report.

This ITC matter is still pending, an evidentiary hearing being scheduled for December, 2006.

This information disclosure statement is submitted under 37 C.F.R. § 1.97(c). The fee of \$180.00 has been authorized via EFS to Deposit Account 502664. The Commissioner is hereby

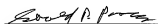
Attorney Docket No.: SNDK.006USS  
FILED VIA EFS

Application No.: 09/129,675

authorized to charge any additional fees, which may be required, or credit any overpayment to Deposit Account 502664.

**FILED VIA EFS**

Respectfully submitted,



Gerald P. Parsons

Reg. No. 24,486

November 22, 2006

Date

PARSONS HSUE & DE RUNTZ LLP

595 Market Street, Suite 1900

San Francisco, CA 94105

(415) 318-1160 (main)

(415) 318-1163 (direct)

(415) 693-0194 (fax)

U.S. Department of Commerce, Patent and Trademark				Atty. Docket No.		Application No.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				SNDK.006USS		09/129,675	
				Applicants		Conf. No.	
(Use several sheets if necessary)				Eliyahou Harari et al.		4949	
(Form PTO-1449)				Filing Date		Art Group	
				August 5, 1998		2824	
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	5,136,541	6/21/1988	Sparks et al.			
	2	4,752,871	8/4/1992	Arakawa			
Foreign Patent Documents							
							Translation
		Document	Date	Country	Class	Subclass	Yes No
	3	JP S 59-121696	7/13/1984	Japan			X
	3	JP H62-6493	1/13/1987	Japan			X
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	5	S. Mehrotra et al., "Serial 9Mb Flash EEPROM for Solid State Disk Applications," 1992 Symposium on VLSI Circuits Digest of Technical Papers, IEEE, pp. 24-25.					
	6	David A. Rich, "A Survey of Multivalued Memories," IEEE Transaction of Computers, Vol. C-35, No. 2, February, 1986, pp. 99-106.					
	7	Clinton Duo et al., "An 80 ns 32K EEPROM Using the FETMOS Cell," IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 5, October, 1982, pp. 821-827.					
	8	Electronic Industries Association, JEDEC Standard, "Configurations for Solid State Memories, JEDEC Standard No. 21-B (Revision of JESD21-A and JESD21-A-1), CS-0336, 337-TA-560, December, 1988, pp. i-36, 39-147.					
	9	"Expert Report of Dr. Richard Pashley Regarding the Invalidity of U.S. Patent No. 5,172,338 and U.S. Patent No. 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 7, 2006, pp. i-387.					
	10	"Rebuttal Expert Report of G.R. Mohan Rao, Ph.D. Regarding Validity and Enforceability of U.S. Patent Nos. 5,172,338 and 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 26, 2006, pp. i-245, Exhibit List, 6 pages.					
	11	"Addendum to the Rebuttal Expert Report of G.R. Mohan Rao, Ph.D. Regarding Validity and Enforceability of U.S. Patent Nos. 5,172,338 and 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 28, 2006, 5 pages.					
Examiner		Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							